

FIG. 1

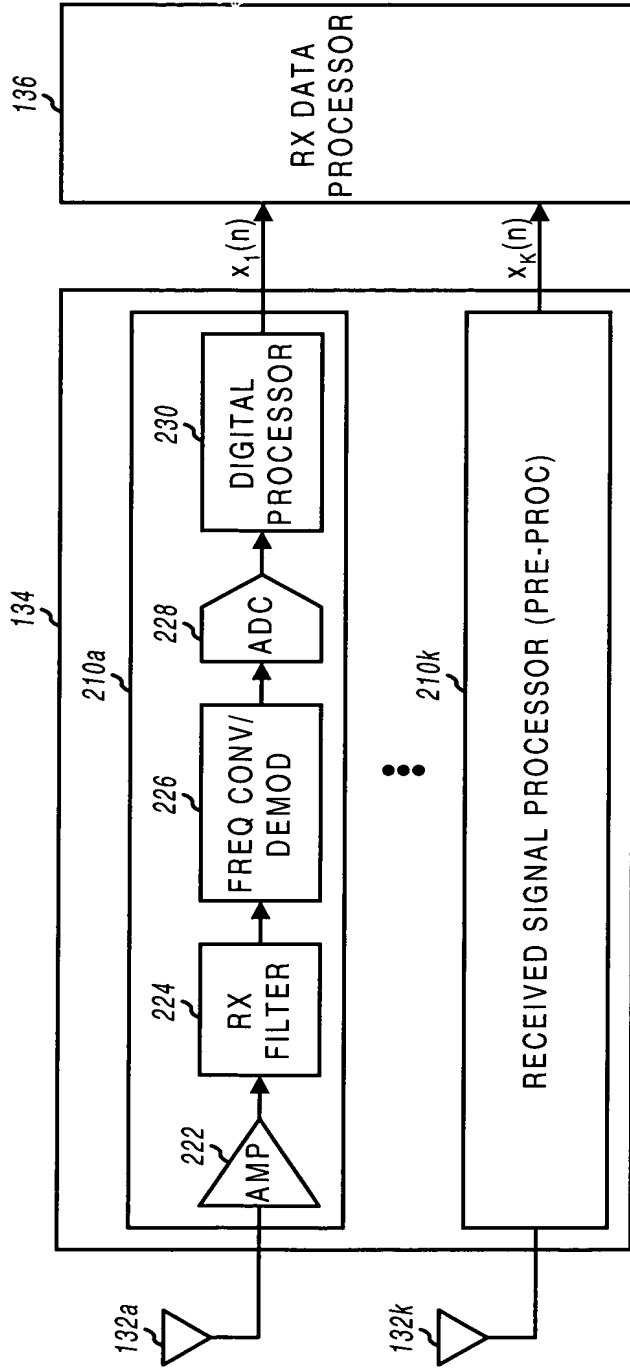


FIG. 2A

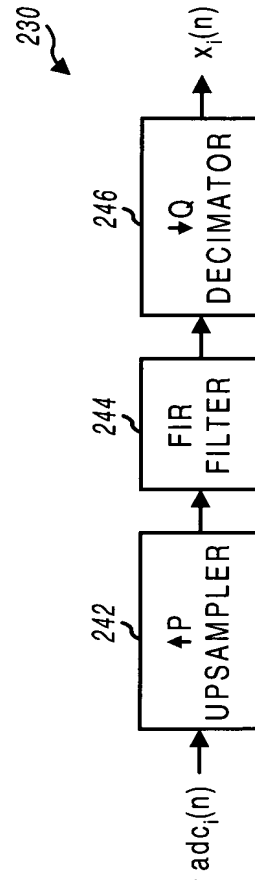


FIG. 2B

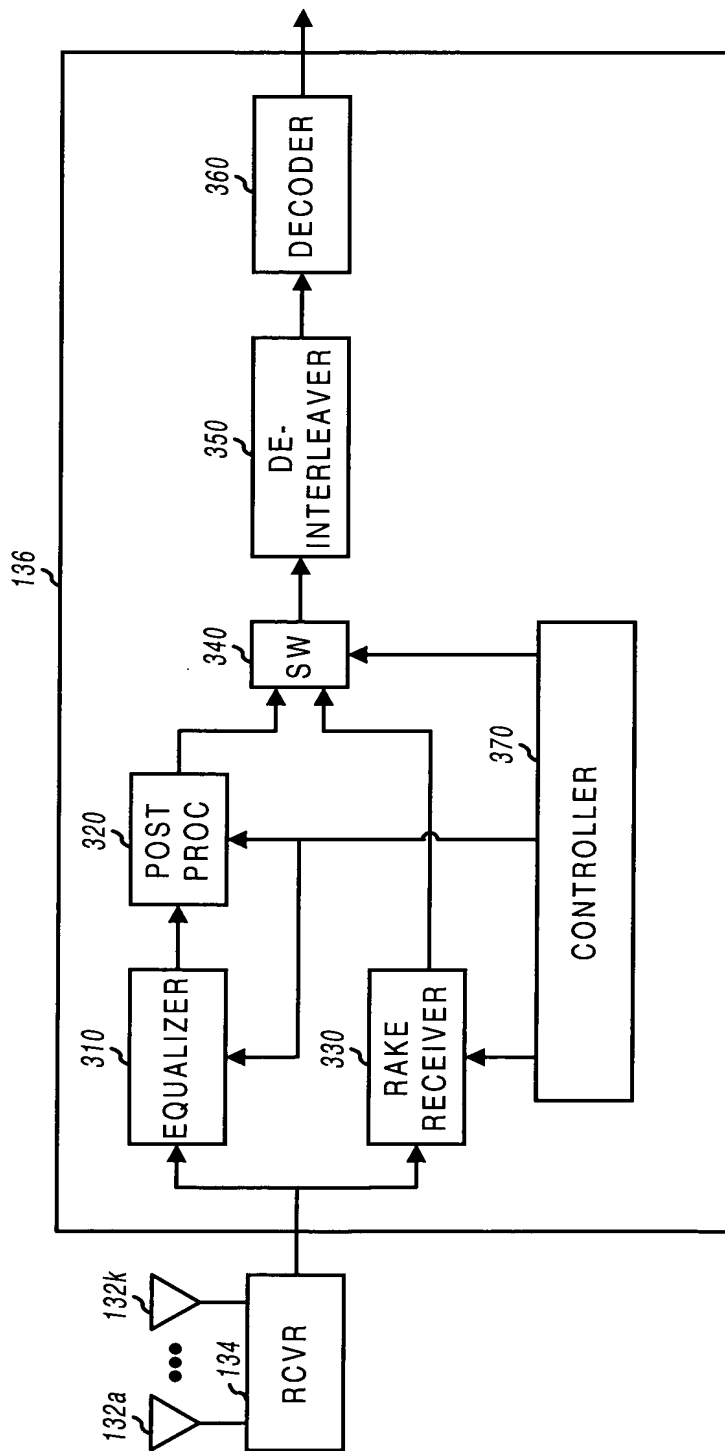


FIG. 3

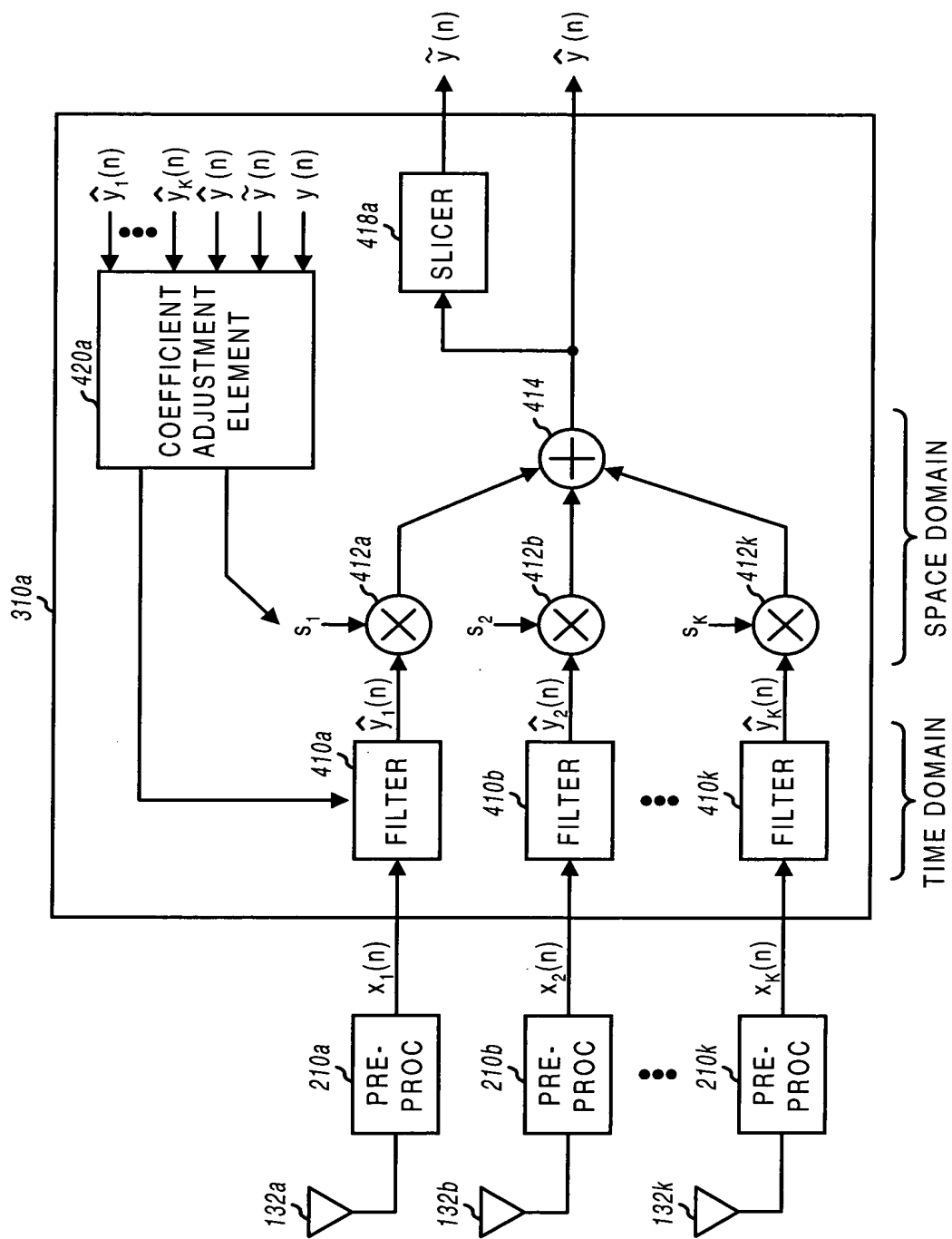


FIG. 4A

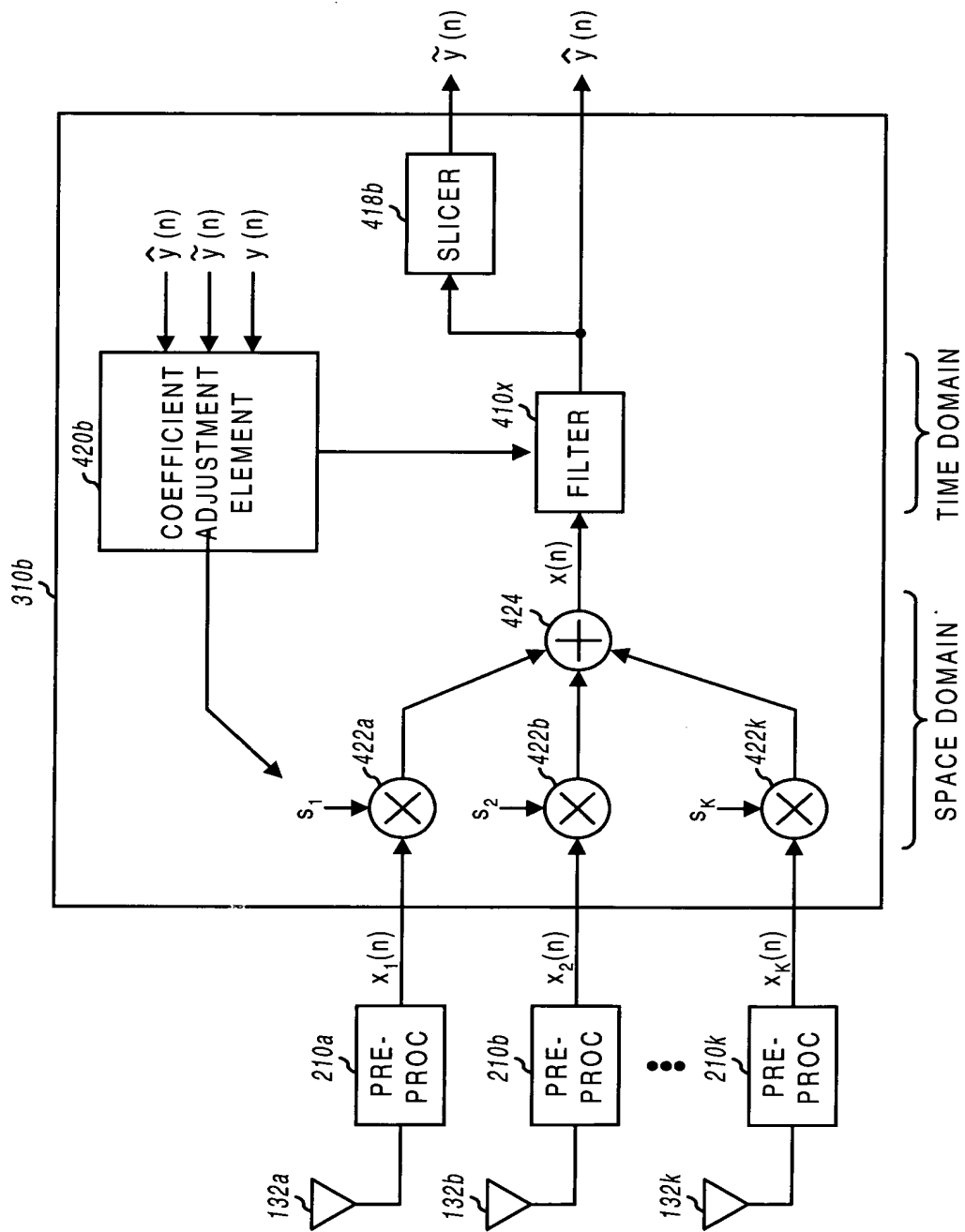


FIG. 4B

410

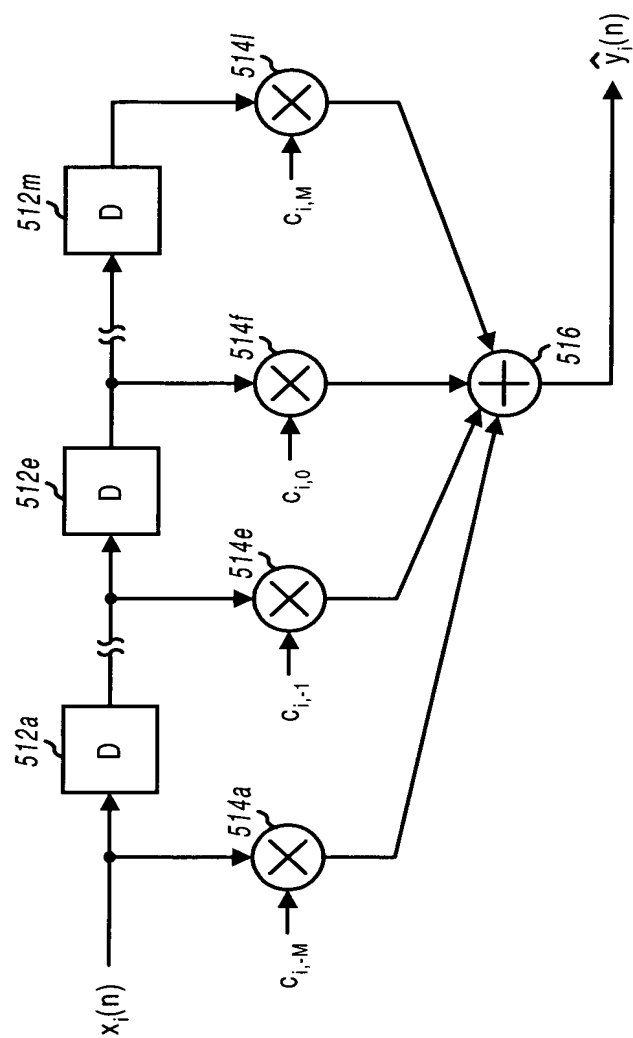


FIG. 5A

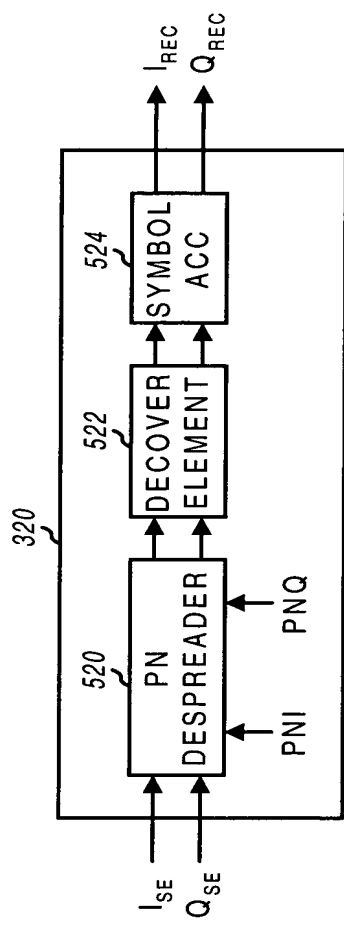


FIG. 5B

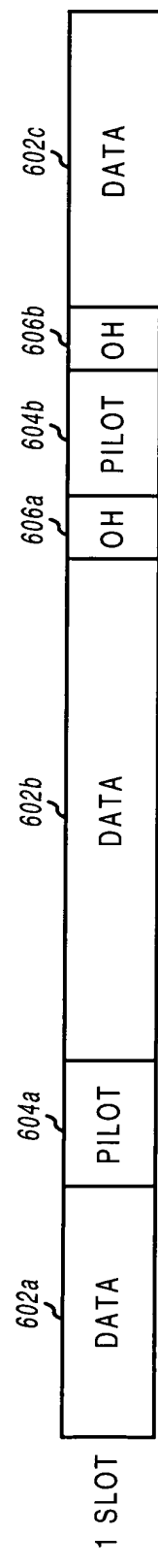


FIG. 6

FIG. 7 is a block diagram of a receiver system 330. The receiver system 330 includes a PN Despreader 720, a Decoder Element 722, a Symbol Accumulator 724, a Pilot Demodulator 726, a Pilot Filter 736, a Symbol Accumulator 734, a Decoder Element 732, a Signal Quality Estimator 750, and a Symbol Combiner 740. The receiver system 330 also includes a Finger Processor 710I and a Searcher Element 712. The receiver system 330 is configured to receive an input signal 330 and process it to produce a signal quality estimate and a combined symbol output.

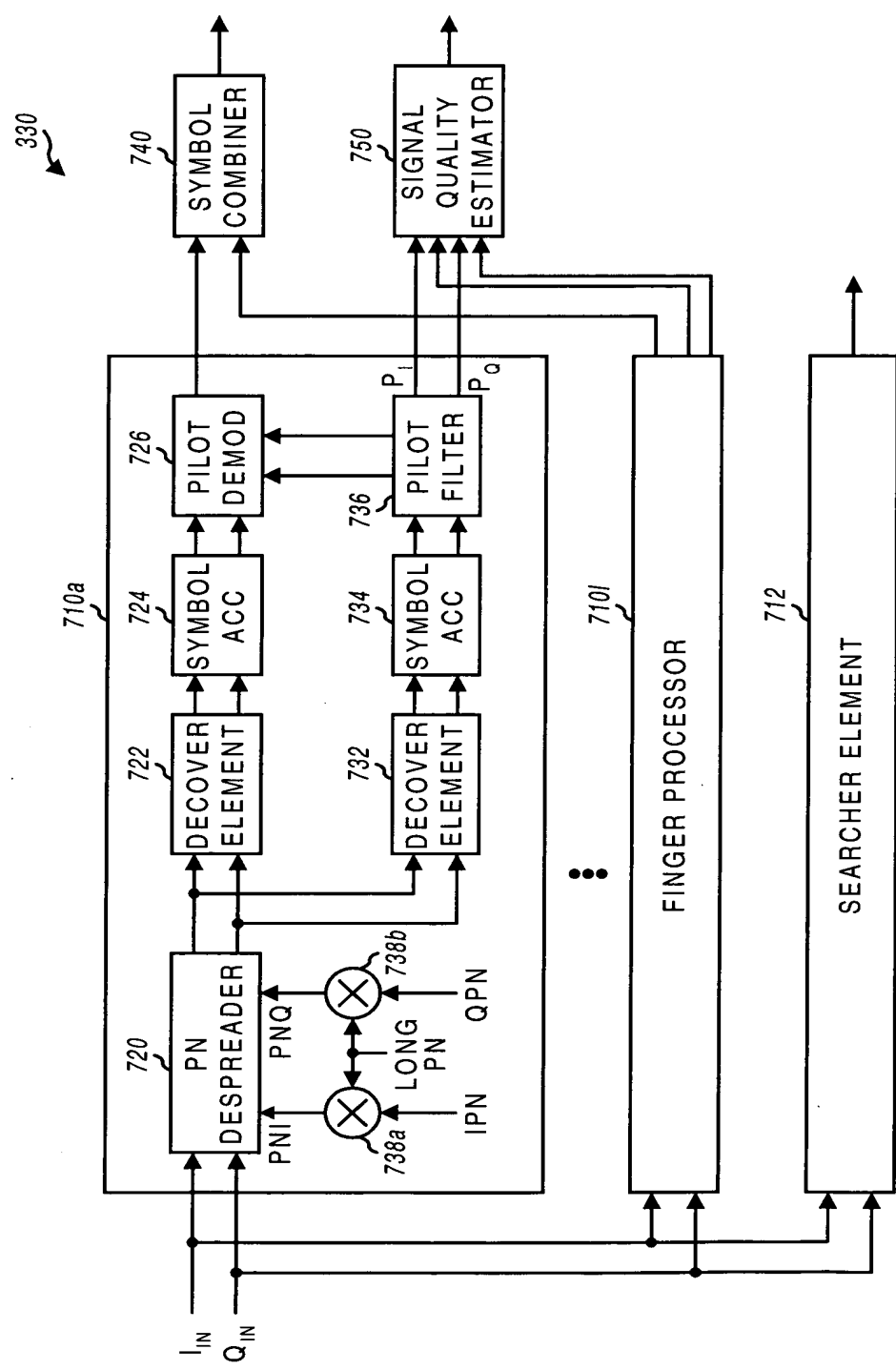


FIG. 7